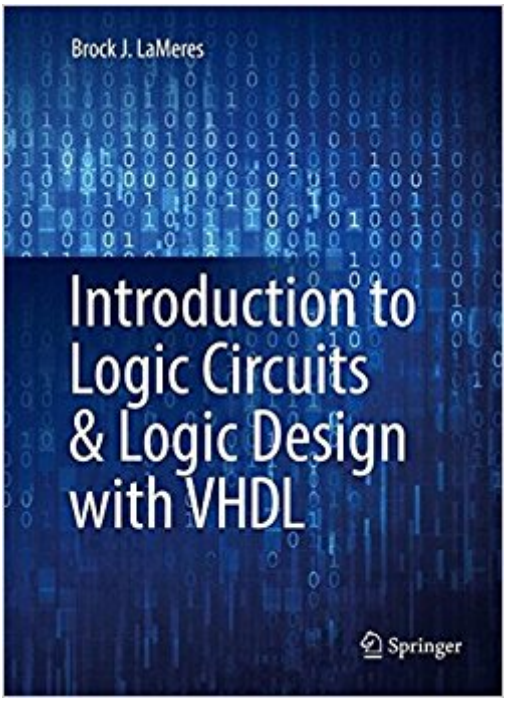
Introduction To Logic Circuits & Logic Design With VHDL



Synopsis

This textbook introduces readers to the fundamental hardware used in modern computers.Ã Â The only pre-requisite is algebra, so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school.Ã Â This book presents both the classical approach to digital system design (i.e., pen and paper) in addition to the modern hardware description language (HDL) design approach (computer-based).Ã Â Ã Â Ã Â This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs.Ã Â This book is designed to match the way the material is actually taught in the classroom. Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics. The author has designed the content with learning goals and assessment at its core.Ã Â Each section addresses a specific learning outcome that the learner should be able to Ã¢â ¬Å“doÃ¢â ¬Â• after its completion. The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome.Ã Â Ã Â This book can be used for either a sequence of two courses consisting of an introduction to logic circuits (Chapters 1-7) followed by logic design (Chapters 8-13) or a single, accelerated course that uses the early chapters as reference material.

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Customer Reviews

Ã¢â ¬Å“Effectively for the undergraduate courses the book can serve the good purpose to

understand the digital terminology and logic circuit design. Ã¢â ¬Â¦ this book is good for beginners and students to understand the digital concepts and basics of VHDL programming.Ã¢â ¬Â• (.com, November, 2016)

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This book lays out the concepts and uses of VHDL better than any other textbook of its kind. It’s easy to follow and one of the rare textbooks that are easy to read. It’s not just page after page of dry technical reading. If you’re looking for a book to learn VHDL this is the one to buy.

Chapter 1 to Chapter 3: Lot of emphasis is given by the authors to cover the basic fundamentals required for the design. Chapter 1 to 4 has the many concept check examples and exercises with meaningful questions. But still it lacks the practicality and clarity of explanation. The application of the logic gates and the practical scenarios need to be discussed in more details during the next

edition. The current industrial standard is low power designs and voltage levels are less than 5 Volt and even 3.3, 2.8 and 1.5 volt. This need to be mentioned in the section 3.3 with the noise margin and driving loads. Effectively for the undergraduate courses the book can serve the good purpose to understand the digital terminology and logic circuit design.Chapter 4 discusses about the combinational logic design and author has covered the concepts in detail with the minimization techniques. For the further improvement in this book, authors can think about adding the area optimization techniques, the parallel logic and concurrent logic and the design performance. Even for better readability the medium complex examples need to be discussed in this chapter.Chapter 5 has information about the basic of VHDL and with reference to the initial chapters the emphasis is given to implement the logic structure. This chapter has VHDL codes for some functionality. Still it lacks the basic difference between structural, behavior and RTL description. For small density logic the approach explained by the authors is good but as design complexity increases the approach is not suitable. So during the next edition it is better to add the different types of VHDL abstractions with the examples.Chapter 6 has VHDL codes and it lacks the naming conventions followed in the practical world. Even the sequence should be MUX, Demux, Advantages and disadvantages with VHDL implementations and then use of decoders, encoders. On page 183-184 there is functional issue in the design of encoder and author should correct this. The encoder all inputs are logic ÃƒÂ¢Ã â ¬Ã Å“0000ÃƒÂ¢Ã â ¬Ã Â• then the output is also ÃƒÂ¢Ã â ¬Ã Å“00ÃƒÂ¢Ã â ¬Ã Â• in the design. So according to the given VHDL description output is ÃƒÂ¢Ã â ¬Ã Å“00ÃƒÂ¢Ã â ¬Ã Â• for input ÃƒÂ¢Ã â ¬Ã Å“0001ÃƒÂ¢Ã â ¬Ã Â• and for when others condition that may be ÃƒÂ¢Ã â ¬Ã Å“0000ÃƒÂ¢Ã â ¬Ã Â•. Under such circumstances it is essential to add the status flag functionality to generate flag output as logic ÃƒÂ¢Ã â ¬Ã Ëœ1ÃƒÂ¢Ã â ¬Ã â„¢ when all inputs of encoder are logic ÃƒÂ¢Ã â ¬Ã Ëœ0ÃƒÂ¢Ã â ¬Ã â„¢.Chapter 7 page 255 has the tmargin delay but the cause of the delay is not explained properly. The Process, Voltage Temperature variation or On-chip-variation need to be explained with some practical example then the timing diagram and the maximum frequency calculation section can be more meaningful and readers will be able to correlate the same with the practical scenarios.Chapter 8: VHDL constructs like if-then-else has potential problem in the description. Please check page 271 code first ÃƒÂ¢Ã â ¬Ã Å“Due to missing ÃƒÂ¢Ã â ¬Ã ËœelseÃƒÂ¢Ã â ¬Ã â„¢ clause it infers the latch. But author has described this as combinational logic. Unnecessary unintentional latches are inferred in the design. In this chapter most of the example lacks the naming conventions. Even the quality of example need to be improved, even there is need to add few practical applications.Chapter 9 has sequential logic VHDL

description; still it lacks the difference between the behavior and RTL. The RTL coding should be improved with the meaningful naming conventions and with some additional complex functionality.Chapter 10 and 11 has only theoretical information about the memories and programmable logic and this can be improved with some practical scenarios and with few examples and realization of VHDL into synthesis.Chapter 12 and 13 covers VHDL coding of adders to processor but still the linking of the concept to implement the design is lacking in this. These two chapters can be improved by adding other concepts related with RTL design, synthesis and design implementation using FPGA or ASIC.Overall the book covers many concepts but lacks the practical scenarios and even it lacks the complex example design, synthesis and implementation. But this book is good for beginners and students to understand the digital concepts and basics of VHDL programming.

Excellent textbook!

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